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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants: ANATOLIY V. TSYRGANOVICH
Assignee: Zilog, Inc.
Title: CIRCLE CORRECTION IN DIGITAL LOW-PASS FILTER
Serial No.: 08/937,877 Filing Date: September 29, 1997
Examiner: Vivek Srivastava Group Art Unit: 2711
Docket No.: 11599 M-10874 [Formerly: ZILG.183US0]

San Jose, California
December 13, 2000

BOX Amendments
ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

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AMENDMENT

Dear Sir:

In response to the Office Action mailed September 27, 2000, please amend the above-identified patent application as follows:

IN THE CLAIMS:

Please amend the following claims as indicated:

1. (Amended) A circuit comprising:
- a first circuit portion connected to a first input, the first circuit portion including at least one delay element; and
 - a second circuit portion attached to the first circuit portion, the second circuit portion including at least one delayed signal input from the at least one delay element, and an adjustment input, the adjustment input not passing through the at least one delay element, wherein when there is no adjustment input, the circuit acts as a filter, [and] wherein the adjustment input changes the level of the output without the adjustment signal being filtered,

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